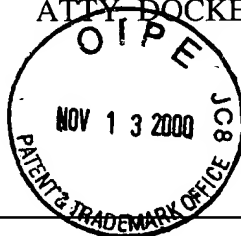


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List of Patents and Publications
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APPLICANT: Dye et al.

FILING DATE: June 13, 2000

GROUP: Unknown

U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
TIV		5,801,689	09/01/98	Huntsman			
TIV		4,901,221	02-13-90	Kodosky et al.			
TIV		4,849,880	7/18/89	Bhaskar et al			
TIV		5,603,043	02-11-97	Taylor et al.			
TIV		5,109,504	04/28/92	Littleton	395	500	
TIV		5,784,275	07/21/98	Sojoodi et al.	364	191	
TIV		5,437,464	08/01/95	Terasima et al.	463	43	
TIV		5,566,295	10/15/96	Cypher et al.	345	326	
TIV		5,724,074	03/1998	Chainani et al.	345	474	
TIV		5,760,788	3/1998	Chainani et al.	345	475	

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EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION. YES/NO
TIV		WO 96 14618 A	5/17/96	PCT			

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

TIV		International Search Report for PCT/US 98/10916 dated 9/17/98.
TIV		Douglas, Bruce D., Ph.D., "Custom Embedded Communication Protocols, Conference Proceedings of Embedded Systems Conference East, March 10-12, 1997, Boston, Massachusetts, pp. 637-657.

EXAMINER: TED T. VO DATE CONSIDERED: 5/14/03

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

Information Disclosure Statement--PTO 1449 (modified)

Form PTO-1449 (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)			ATTY DOCKET NO: 5150-38601 <div style="text-align: center;">  </div>		SERIAL NO: 096 <div style="text-align: center;"> RECEIVED NOV 20 2000 Technology Center 2100 </div>		
			APPLICANT: Dye et al.				
			FILING DATE: June 13, 2000		GROUP: Unknown		
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EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
FOREIGN PATENT DOCUMENTS							
EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION. YES/NO
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
TTU		Labrosse, Jean, "Inside Real-Time Kernels," Conference Proceedings of Embedded Systems Conference East, March 10-12, 1997, Boston, Massachusetts, pp. 205-214.					
TTU		Labrosse, Jean, "Designing with Real-Time Kernels," Conference Proceedings of Embedded Systems Conference East, March 10-12, 1997, Boston, Massachusetts, pp. 379-389.					
TTU		Operating Systems, 2 nd Ed. By Williams Stallings, Prentice Hall, 1995, "Multiprocessor and Real-Time Scheduling", pp. 394-415.					
TTU		Shearman, Sam, Assoc. Editor, "Hardware-in-the-loop connectivity extends continuous-system simulation", Personal Engineering, June 1997, pp. 18-37.					
TTU		XP0002087124 Xiao-Yu, et al., "Software Environment for WASMII: A Data Driven Machine with a Virtual Hardware," Field Programmable Logic Architectures, Synthesis and Applications, 4 th International Workshop on Field-Programmable Logic and Applications, FPL '94 Proceedings, Berlin, Germany, 1994.					
TTU		XP000554820 Edwards, et al., "Software acceleration using programmable hardware devices," IEEE Proceedings: Computers and Digital Techniques, vol. 143, no. 1, January 1996, pp. 55-63.					
TTU		XP000380758 Lesser, et al, "High Level Synthesis and Generating FPGAs with the BEDROC System," Journal of VLSI Signal Processing, vol. 6, no. 2, August 1993, pp. 191-214.					
TTU		International Search Report for PCT/US 98/13040 mailed 12/22/98.					
TTU		"LabVIEW USER MANUAL for Windows", National Instruments Co., 09/1994 pp 4/1-21.					
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

TTU	Beguelin et al., "Visualization and Debugging in a Heterogenous Environment", IEEE, 06/1993, pp 88-95.
TTU	Jamal et al., "The Aplicability of the Visual Programming Language LabVIEW to Large Real-World Application", IEEE, 1995, pp 99-106.
TTU	Per-Arne Wiberg, "Graphical Programming of Time-Deterministic Real-Time Systems", IEEE, 1996, pp 173-180.
TTU	Ade, M; Lauwereins, R; Peperstraete, J.A.; Hardware-Software Codesign with GRAPE, Proceedings of the Sixth IEEE International Workshop on Rapid System Prototyping, 6/9/95, pp. 40-47.
TTU	Lauwereins, R; Engels, M; Ade, M; Peperstraete, J; Grape-II: A System-Level Prototyping Envrionment for DSP Applications, Computer, Vol. 28, Issue 2, 2/95, pp. 35-43.

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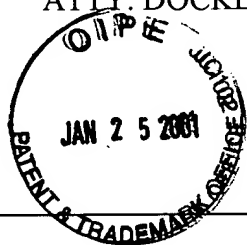
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FOREIGN PATENT DOCUMENTS

EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION. YES/NO

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

TIV	B1	Weban, A.; Brown, G. "A Software Development System for FPGA-based Data Acquisition Systems", FPGAs for Custom Computing Machines, 1996.					

XAMINER: TED T. VO

DATE CONSIDERED: 5/14/03

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FOREIGN PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
TIV	C1	0 367 709	5/1990	EP			
TIV	C2	0 398 646	11/1990	EP			
TIV	C3	0 426 909	5/1991	EP			

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

FTV	C4	International Search Report, Application No. PCT/US 00/22317, mailed December 19, 2000.

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